Interface Processing ! FIG Memory Pevice

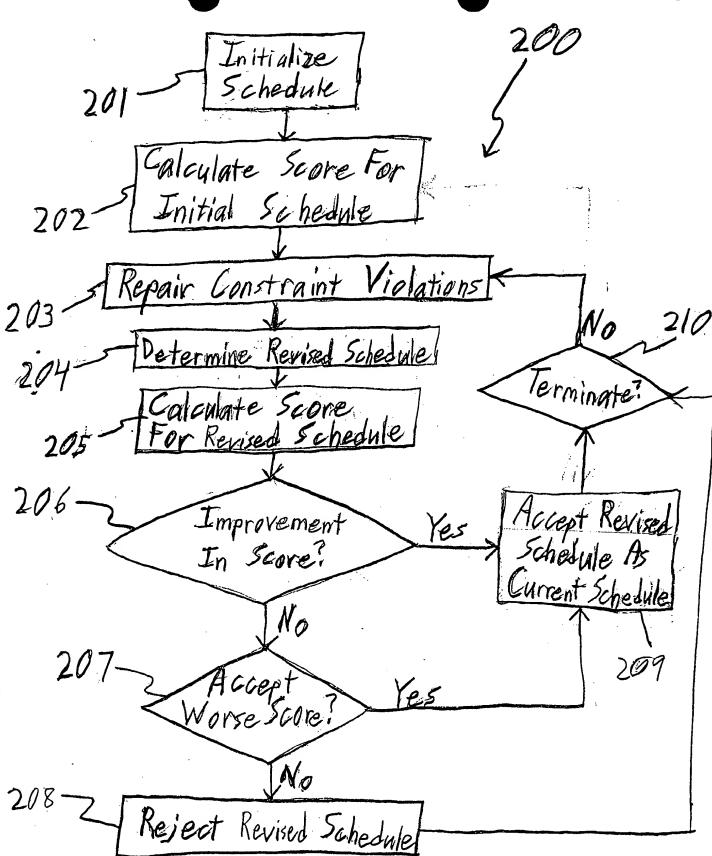
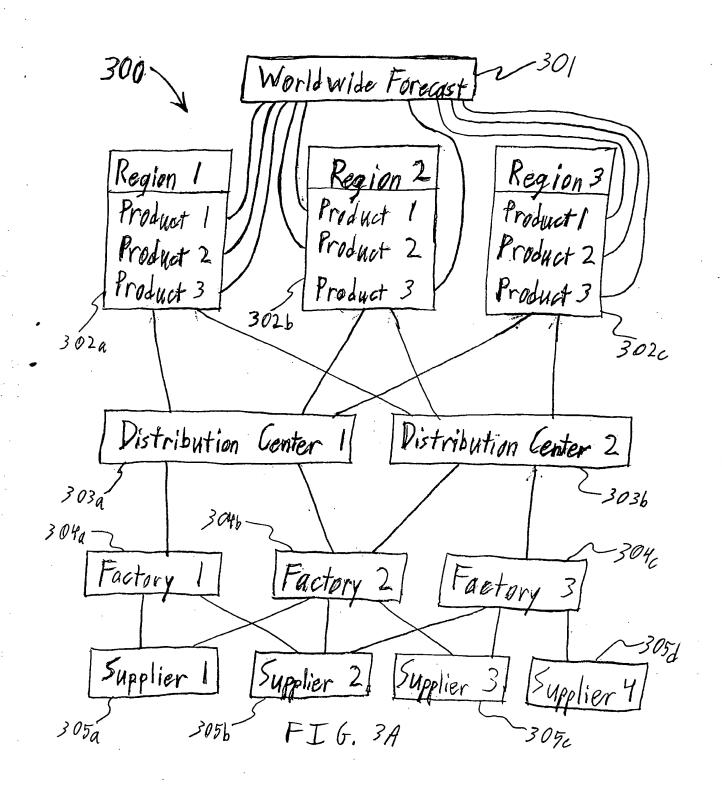
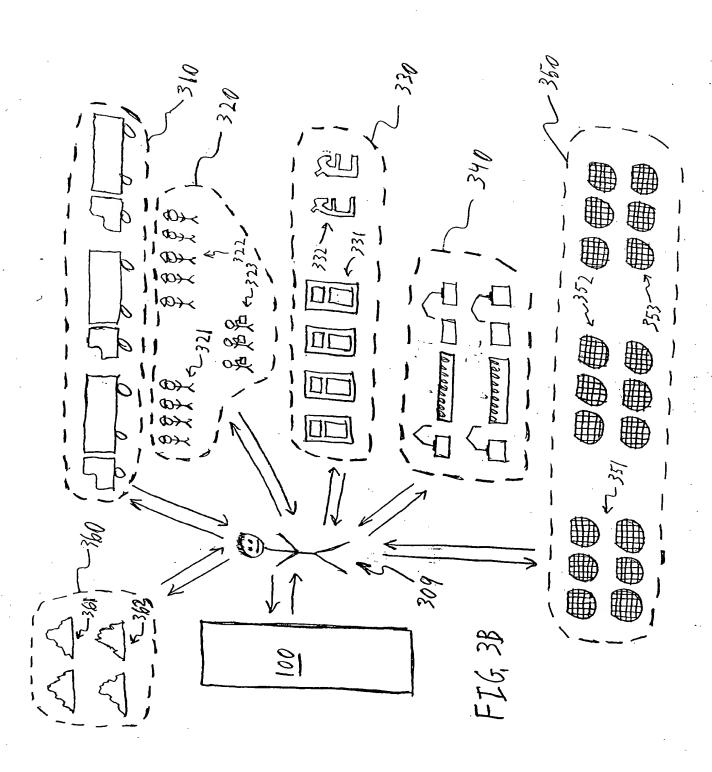


FIG. 2





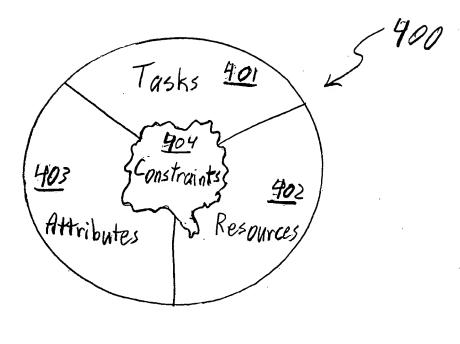


FIG. 4A

